## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Art Unit: Not assigned

Kuniaki KONNO et al.

Examiner: Not assigned

Serial No: Not assigned

Filed: September 26, 2003

For: SEMICONDUCTOR LIGHT EMITTING DIODE

## TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The information disclosure statement submitted herewith is being filed concurrently with the subject application [37 C.F.R. § 1.97(b)] and contains no items of information cited in any communication from a foreign patent office in a counterpart foreign application [37 C.F.R. § 1.97(e)(1)].

If it should be determined that for any reason either an insufficient or excessive fee has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 50-1314. A copy of this paper is enclosed.

Respectfully submitted,

Date: September 26, 2003

Anthony J. Orler

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Facsimile: 213-337-6701

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Date: September 26, 2003

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			Applicant  Kuniaki KONNO et al.			
			Filing Date September 26, 2003		Group Art Unit Not assigned	
		U.S. PATEN	T DOCUMENTS			
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,793,062	08/11/98	Kish et al.			
	2002/0185648 A1	12/12/02	Furukawa et al.			
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	F	DREIGN PAT	ENT DOCUMENTS	3	-	300
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION (YES/NO)
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	OTHER DOCUMEN	TS (Including A	uthor, Title, Date, Per	tinent Pages,	Etc.)	
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